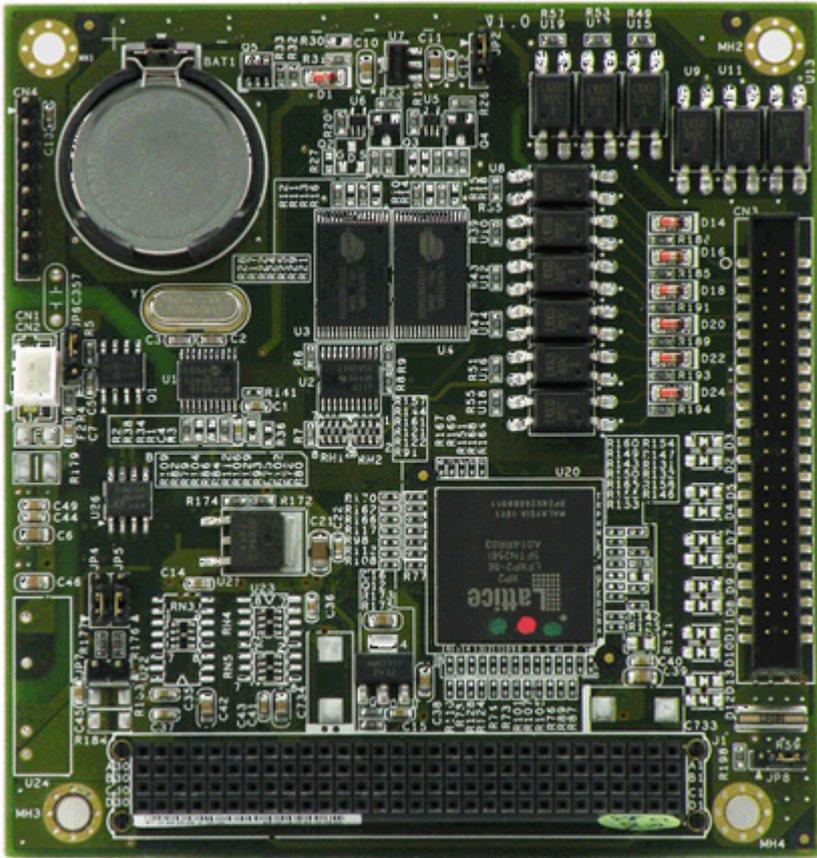


39.00 EUR  
incl. 19% VAT, plus [shipping](#)

- CAN Bus !
- 12 channel I/O !



Support: [Technical Notes](#)

CAN-BUS PCI-104 controller card for FleetPC-3 (not -B !).

|                |   |
|----------------|---|
|                | PCI104 digital I/O, SRAM disk & CAN bus module  |
| PCB            | 4-layer PCB   |
| <b>General</b> |   |
| Bus interface  | PCI 104, PCI 2.0 compliant  |
| Controller     | FPGA & Standalone CAN controller  |
| SRAM disk      | <ul style="list-style-type: none"> <li>- 2 x 512KB low power SRAM</li> <li>- 1M Byte as one bank</li> <li>- Battery backup by optional module</li> <li>- Battery power consumption: less than 15uA</li> <li>- Operation modes: <ul style="list-style-type: none"> <li>A. Memory Mode <ul style="list-style-type: none"> <li>i. Independent mode</li> <li>ii. Replicate mode</li> </ul> </li> <li>B. Disk Mode (is only supported in Linux)</li> <li>C. Mode selection through Jumper (factory default disk mode)</li> </ul> </li> </ul> |

|                |  |
|----------------|--|
| Digital Input  | <ul style="list-style-type: none"> <li>- 12 channels</li> <li>- Internal pull up</li> <li>- Programmable de-bounce time (0 ms to 255ms, 1 ms resolution). True after X ms of constant state.</li> <li>- Support Change of State interrupt</li> <li>- 5000Vrms optical isolation</li> <li>- Response time: 20uS (without de-bounce)</li> <li>- Rising trigger or falling trigger</li> <li>- Suggested maximum input frequency 10KHz( duty = 50% ).</li> <li>- Signal input :             <ul style="list-style-type: none"> <li>A. Open/Ground switch input</li> <li>B. Digital Logic                 <ul style="list-style-type: none"> <li>i. Logic High: 3V to 28V</li> <li>ii. Logic Low: 0V to 1.5V</li> </ul> </li> </ul> </li> </ul> |
| Digital Output | <ul style="list-style-type: none"> <li>- 12 channels</li> <li>- Output Type: Open drain MOSFET driver</li> <li>- Output voltage range: 5V to 30V</li> <li>- Sink Current: maximum 500mA each channel</li> <li>- Power on initial state: MOSFET off</li> <li>- Support pulse generator :             <ul style="list-style-type: none"> <li>A. Programmable cycle time, duty cycle and number of cycles. User defines on and off periods (maximum 8-bit for on and off period value).</li> <li>B. Maximum 65535 cycles</li> <li>C. RUN &amp; STOP command</li> <li>D. Resolution: 1 ms, 100ms and 1 second</li> </ul> </li> </ul>   |
| Timer          | <ul style="list-style-type: none"> <li>- 12 x independent 16-bit timers</li> <li>- Support Time Out Interrupt</li> <li>- Resolution: 1 ms and 100ms second(Resolution: 1ms, and 100ms)</li> </ul>  |
| Counter        | <ul style="list-style-type: none"> <li>- 12 x independent 16-bit counters</li> <li>- Connect to all digital inputs</li> <li>- Operation Mode:             <ul style="list-style-type: none"> <li>a. Count to number interrupt.</li> <li>b. Read and clear</li> <li>c. Read on the fly</li> <li>d. Auto stop counting after programmable constant state interval(Interrupt active after programmable constant state interval Resolution: 1ms, and 100ms)</li> <li>e. Count over to target interrupt.</li> </ul> </li> </ul>   |
| CAN bus        | <ul style="list-style-type: none"> <li>- Connect to FPGA SPI bus</li> <li>- 1 x CAN bus</li> <li>- 2KV isolation</li> <li>- CAN 2.0B Active protocol</li> <li>- Controller: Microchip MCP2515(Industrial grade -40 to 85'C)</li> <li>- Transceiver: Micro chip MCP2551(Industrial grade -40 to 85'C)</li> <li>[Other Transceiver manufacturers: Philips, TI, Maxim, ST, Infineon, Atmel]</li> <li>- 2 pin JST connector(2 pin JST 2.0mm connector )</li> <li>- Programmable baud rate: from 5K bps Maximum 1M bps or user-defined baud rate</li> <li>- Time stamp of CAN message</li> <li>- API library for user development</li> <li>- CAN bus device status query</li> </ul>   |
| Power input    | From PCI 104   |

|                        |  |
|------------------------|--|
| Maximum card           | Maximum 2 cards can be stacked up in one system  |
| Jumper                 | - INT# & ID select. Please see Appendix.<br>- SRAM chip capacity select (Used for when auto detection doesn't work only)   |
| Digital I/O connector  | - 44 pin 2.0 mm pitch 180 degree with box<br>- Pin Assignment: Appendix 3(Pin assignment modify)   |
| Software               | - Windows XP, XPe and Linux device driver and API<br>- Windows XP, XPe and Linux demo program<br>- User interface for DIO, SRAM and CAN bus in Linux and Windows XP embedded |
| <b>Mechanical</b>      |  |
| Dimensions             | 90.17 x 95.89mm (3.55"x3.775")   |
| Operating temperature  | -20oC to 70oC (-1~158oF) without air flow  |
| Storage temperature    | -20~85oC (-4~185oF)  |
| Relative Humidity      | 0 to 90% @ 40°C, non-condensing (95% @ 40°C, Non-Condensing by request)  |
| <b>Scope of supply</b> |  |
| 1x                     | PCI 104 Controller card  |
| 1x                     | 150mm Digital I/O cable  |
| 1x                     | 150mm 2-wire cable for CAN bus   |